Specification

Title of the Invention

Speech Signal Decoding Method and Apparatus

5   Background of the Invention

The present invention relates to encoding and
decoding apparatuses for transmitting a speech signal at
a low bit rate and, more particularly, to a speech
signal decoding method and apparatus for improving the

10   quality of unvoiced speech.

As a popular method of encoding a speech
signal at low and middle bit rates with high efficiency,
a speech signal is divided into a signal for a linear
predictive filter and its driving sound source signal

15   (sound source signal). One of the typical methods is
CELP (Code Excited Linear Prediction). CELP obtains a
synthesized speech signal (reconstructed signal) by
driving a linear prediction filter having a linear
prediction coefficient representing the frequency

20   characteristics of input speech by an excitation signal
given by the sum of a pitch signal representing the
pitch period of speech and a sound source signal made up
of a random number and a pulse. CELP is described in M.
Schroeder et al., "Code-excited linear prediction:

25   High-quality speech at very low bit rates", Proc. of
IEEE Int. Conf. on Acoust., Speech and Signal Processing,
pp. 937 - 940, 1985 (reference 1).

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Mobile communications such as portable phones require high speech communication quality in noise environments represented by a crowded street of a city and a driving automobile. Speech coding based on the above-mentioned CELP suffers deterioration in the quality of speech (background noise speech) on which noise is superposed. To improve the encoding quality of background noise speech, the gain of a sound source signal is smoothed in the decoder.

A method of smoothing the gain of a sound source signal is described in "Digital Cellular Telecommunication System; Adaptive Multi-Rate Speech Transcoding", ETSI Technical Report, GSM 06.90 version 2.0.0, January 1999 (reference 2).

Fig. 4 shows an example of a conventional speech signal decoding apparatus for improving the coding quality of background noise speech by smoothing the gain of a sound source signal. A bit stream is input at a period (frame) of T_{fr} msec (e.g., 20 msec), and a reconstructed vector is calculated at a period (subframe) of T_{fr}/N_{sfr} msec (e.g., 5 msec) for an integer N_{sfr} (e.g., 4). The frame length is given by L_{fr} samples (e.g., 320 samples), and the subframe length is given by L_{sfr} samples (e.g., 80 samples). These numbers of samples are determined by the sampling frequency (e.g., 16 kHz) of an input signal. Each block will be described.

The code of a bit stream is input from an input terminal 10. A code input circuit 1010 segments the code of the bit stream input from the input terminal 10 into several segments, and converts them into indices

5 corresponding to a plurality of decoding parameters. The code input circuit 1010 outputs an index corresponding to LSP (Linear Spectrum Pair) representing the frequency characteristics of the input signal to an LSP decoding circuit 1020. The circuit 1010 outputs an

10 index corresponding to a delay L_{pd} representing the pitch period of the input signal to a pitch signal decoding circuit 1210, and an index corresponding to a sound source vector made up of a random number and a pulse to a sound source signal decoding circuit 1110. The

15 circuit 1010 outputs an index corresponding to the first gain to a first gain decoding circuit 1220, and an index corresponding to the second gain to a second gain decoding circuit 1120.

The LSP decoding circuit 1020 has a table

20 which stores a plurality of sets of LSPs. The LSP decoding circuit 1020 receives the index output from the code input circuit 1010, reads an LSP corresponding to the index from the table, and sets the LSP as $LSP\hat{q}_j^{(N_{sfr})}(n)$, $j = 1, \Lambda, N_p$ in the N_{sfr}th subframe of the current frame

25 (nth frame). N_p is a linear prediction order. The LSPs of the first to $(N_{sfr}-1)$th subframes are obtained by linearly interpolating $\hat{q}_j^{(N_{sfr})}(n)$ and $\hat{q}_j^{(N_{sfr})}(n - 1)$. $LSP\hat{q}_j^{(m)}(n)$,

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$j = 1, \Lambda, N_p$, $m = 1, \Lambda, N_{sfr}$ are output to a linear prediction coefficient conversion circuit 1030 and smoothing coefficient calculation circuit 1310.

The linear prediction coefficient conversion circuit 1030 receives $LSP\hat{q}_j^{(m)}(n)$, $j = 1, \Lambda, N_p$, $m = 1, \Lambda, N_{sfr}$ output from the LSP decoding circuit 1020. The linear prediction coefficient conversion circuit 1030 converts the received $\hat{q}_j^{(m)}(n)$ into a linear prediction coefficient $\hat{\alpha}_j^{(m)}(n)$, $j = 1, \Lambda, N_p$, $m = 1, \Lambda, N_{sfr}$, and outputs $\hat{\alpha}_j^{(m)}(n)$ to a synthesis filter 1040. Conversion of the LSP into the linear prediction coefficient can adopt a known method, e.g., a method described in Section 5.2.4 of reference 2.

The sound source signal decoding circuit 1110 has a table which stores a plurality of sound source vectors. The sound source signal decoding circuit 1110 receives the index output from the code input circuit 1010, reads a sound source vector corresponding to the index from the table, and outputs the vector to a second gain circuit 1130.

The second gain decoding circuit 1120 has a table which stores a plurality of gains. The second gain decoding circuit 1120 receives the index output from the code input circuit 1010, reads a second gain corresponding to the index from the table, and outputs the second gain to a smoothing circuit 1320.

The second gain circuit 1130 receives the first sound source vector output from the sound source

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signal decoding circuit 1110 and the second gain output from the smoothing circuit 1320, multiplies the first sound source vector and the second gain to decode a second sound source vector, and outputs the decoded

5 second sound source vector to an adder 1050.

A storage circuit 1240 receives and holds an excitation vector from the adder 1050. The storage circuit 1240 outputs an excitation vector which was input and has been held to the pitch signal decoding

10 circuit 1210.

The pitch signal decoding circuit 1210 receives the past excitation vector held by the storage circuit 1240 and the index output from the code input circuit 1010. The index designates the delay L_{pd}. The

15 pitch signal decoding circuit 1210 extracts a vector for L_{sfr} samples corresponding to the vector length from the start point of the current frame to a past point by L_{pd} samples in the past excitation vector. Then, the circuit 1210 decodes a first pitch signal (vector). For

20 $L_{pd} < L_{sfr}$, the circuit 1210 extracts a vector for L_{pd} samples, and repetitively couples the extracted L_{pd} samples to decode the first pitch vector having a vector length of L_{sfr} samples. The pitch signal decoding circuit 1210 outputs the first pitch vector to a first

25 gain circuit 1230.

The first gain decoding circuit 1220 has a table which stores a plurality of gains. The first gain

decoding circuit 1220 receives the index output from the code input circuit 1010, reads a first gain corresponding to the index, and outputs the first gain to the first gain circuit 1230.

The first gain circuit 1230 receives the first pitch vector output from the pitch signal decoding circuit 1210 and the first gain output from the first gain decoding circuit 1220, multiplies the first pitch vector and the first gain to generate a second pitch vector, and outputs the generated second pitch vector to the adder 1050.

The adder 1050 receives the second pitch vector output from the first gain circuit 1230 and the second sound source vector output from the second gain circuit 1130, adds them, and outputs the sum as an excitation vector to the synthesis filter 1040.

The smoothing coefficient calculation circuit 1310 receives LSP$\hat{q}_j^{(m)}(n)$ output from the LSP decoding circuit 1020, and calculates an average LSP$\overline{q}_{0j}(n)$:

$$\overline{q}_{0j}(n) = 0.84 \cdot \overline{q}_{0j}(n - 1) + 0.16 \cdot \hat{q}_j^{(N_{sfr})}(n)$$

The smoothing coefficient calculation circuit 1310 calculates an LSP variation amount $d_0(m)$ for each subframe m:

$$d_0(m) = \sum_{j=1}^{N_P} \frac{\left| \overline{q}_{0j}(n) - \hat{q}_j^{(m)}(n) \right|}{\overline{q}_{0j}(n)}$$

The smoothing coefficient calculation circuit 1310 calculates a smoothing coefficient $k_0(m)$ of the subframe

m:

$$k_0(m) = \min(0.25, \ \max(0, d_0(m) - 0.4))/0.25$$

where $\min(x, y)$ is a function using a smaller one of x
and y, and $\max(x, y)$ is a function using a larger one of
x and y. The smoothing coefficient calculation circuit
1310 outputs the smoothing coefficient $k_0(m)$ to the
smoothing circuit 1320.

The smoothing circuit 1320 receives the
smoothing coefficient $k_0(m)$ output from the smoothing
coefficient calculation circuit 1310 and the second gain
output from the second gain decoding circuit 1120. The
smoothing circuit 1320 calculates an average gain $\overline{g}_0(m)$
from a second gain $\hat{g}_0(m)$ of the subframe m by

$$\overline{g}_0(m) = \frac{1}{5} \sum_{i=0}^{4} \hat{g}_0(m - i)$$

The second gain $\hat{g}_0(m)$ is replaced by

$$\hat{g}_0(m) = \hat{g}_0(m) \cdot k_0(m) + \overline{g}_0(m) \cdot (1 - k_0(m))$$

The smoothing circuit 1320 outputs the second
gain $\hat{g}_0(m)$ to the second gain circuit 1130.

The synthesis filter 1040 receives the
excitation vector output from the adder 1050 and a
linear prediction coefficient $\alpha_i, \ i = 1, \Lambda, N_p$ output from
the linear prediction coefficient conversion circuit
1030. The synthesis filter 1040 calculates a
reconstructed vector by driving the synthesis filter
$1/A(z)$ in which the linear prediction coefficient is set,
by the excitation vector. Then, the synthesis filter

1040 outputs the reconstructed vector from an output
terminal 20. Letting α_i, i = 1,Λ,N_p be the linear
prediction coefficient, the transfer function 1/A(z) of
the synthesis filter is given by

5
$$1 / (A)z = 1 / (1 - \sum_{i=1}^{N_p} \alpha_i z^i)$$

Fig. 5 shows the arrangement of a speech
signal encoding apparatus in a conventional speech
signal encoding/decoding apparatus. A first gain
circuit 1230, second gain circuit 1130, adder 1050, and

10 storage circuit 1240 are the same as the blocks
described in the conventional speech signal decoding
apparatus in Fig. 4, and a description thereof will be
omitted.

An input signal (input vector) generated by

15 sampling a speech signal and combining a plurality of
samples as one frame into one vector is input from an
input terminal 30. A linear prediction coefficient
calculation circuit 5510 receives the input vector from
the input terminal 30. The linear prediction

20 coefficient calculation circuit 5510 performs linear
prediction analysis for the input vector to obtain a
linear prediction coefficient. Linear prediction
analysis is described in Chapter 8 "Linear Predictive
Coding of Speech" of reference 4.

25 The linear prediction coefficient calculation
circuit 5510 outputs the linear prediction coefficient

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to an LSP conversion/quantization circuit 5520, weighting filter 5050, and weighting synthesis filter 5040.

The LSP conversion/quantization circuit 5520
5   receives the linear prediction coefficient output from
the linear prediction coefficient calculation circuit
5510, converts the linear prediction coefficient into
LSP, and quantizes the LSP to attain the quantized LSP.
Conversion of the linear prediction coefficient into the
10 LSP can adopt a known method, e.g., a method described
in Section 5.2.4 of reference 2.

Quantization of the LSP can adopt a method
described in Section 5.2.5 of reference 2. As described
in the LSP decoding circuit of Fig. 4 (prior art), the
15 quantized LSP is the quantized $\mathrm{LSP}\hat{q}_j^{(N_{sfr})}(n)$, $j = 1, \Lambda, N_p$ in
the N_{sfr} subframe of the current frame (nth frame). The
quantized LSPs of the first to $(N_{sfr}-1)$th subframes are
obtained by linearly interpolating $\hat{q}_j^{(N_{sfr})}(n)$ and
$\hat{q}_j^{(N_{sfr})}(n - 1)$. The LSP is $\mathrm{LSP}q_j^{(N_{sfr})}(n)$, $j = 1, \Lambda, N_p$ in the
20 N_{sfr} subframe of the current frame (nth frame). The LSPs
of the first to $(N_{sfr}-1)$th subframes are obtained by
linearly interpolating $q_j^{(N_{sfr})}(n)$ and $q_j^{(N_{sfr})}(n - 1)$.

The LSP conversion/quantization circuit 5520
outputs the $\mathrm{LSP}q_j^{(m)}(n)$, $j = 1, \Lambda, N_p$, $m = 1, \Lambda, N_{sfr}$, and the
25 quantized $\mathrm{LSP}\hat{q}_j^{(m)}(n)$, $j = 1, \Lambda, N_p$, $m = 1, \Lambda, N_{sfr}$ to a linear
prediction coefficient conversion circuit 5030, and an
index corresponding to the quantized $\mathrm{LSP}\hat{q}_j^{(N_{sfr})}(n)$, $j =$

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$1, \Lambda, N_p$ to a code output circuit 6010.

The linear prediction coefficient conversion circuit 5030 receives the $\text{LSP}q_j^{(m)}(n)$, $j = 1, \Lambda, N_p$, $m = 1, \Lambda, N_{sfr}$, and the quantized $\text{LSP}\hat{q}_j^{(m)}(n)$, $j = 1, \Lambda, N_p$, $m =$

5      $1, \Lambda, N_{sfr}$ output from the LSP conversion/quantization circuit 5520. The circuit 5030 converts $q_j^{(m)}(n)$ into a linear prediction coefficient $\alpha_j^{(m)}(n)$, $j = 1, \Lambda, N_p$, $m = 1, \Lambda, N_{sfr}$, and $\hat{q}_j^{(m)}(n)$ into a quantized linear prediction coefficient $\hat{\alpha}_j^{(m)}(n)$, $j = 1, \Lambda, N_p$, $m = 1, \Lambda, N_{sfr}$. The linear

10   prediction coefficient conversion circuit 5030 outputs the $\alpha_j^{(m)}(n)$ to the weighting filter 5050 and weighting synthesis filter 5040, and $\hat{\alpha}_j^{(m)}(n)$ to the weighting synthesis filter 5040. Conversion of the LSP into the linear prediction coefficient and conversion of the quantized LSP into the quantized linear prediction

15   coefficient can adopt a known method, e.g., a method described in Section 5.2.4 of reference 2.

The weighting filter 5050 receives the input vector from the input terminal 30 and the linear prediction coefficient output from the linear prediction

20   coefficient conversion circuit 5030, and generates a weighting filter W(z) corresponding to the human sense of hearing using the linear prediction coefficient. The weighting filter is driven by the input vector to obtain a weighted input vector. The weighting filter 5050

25   outputs the weighted input vector to a subtractor 5060. The transfer function W(z) of the weighting filter 5050

is given by $W(z) = Q(z/\gamma_1)/Q(z/\gamma_2)$.

Note that $Q(z/\gamma_1) = 1 - \sum_{i=1}^{N_p} \alpha_i^{(m)}\gamma_1^i z^i$ and $Q(z/\gamma_2)$

$= 1 - \sum_{i=1}^{N_p} \alpha_i^{(m)}\gamma_2^i z^i$ where γ_1 and γ_2 are constants, e.g., $\gamma_1 =$

0.9 and $\gamma_2 = 0.6$. Details of the weighting filter are

5   described in reference 1.

The weighting synthesis filter 5040 receives
the excitation vector output from the adder 1050, and
the linear prediction coefficient $\alpha_j^{(m)}(n)$, $j = 1,\Lambda,N_p$, $m =$
$1,\Lambda,N_{sfr}$, and the quantized linear prediction coefficient
10   $\hat{\alpha}_j^{(m)}(n)$, $j = 1,\Lambda,N_p$, $m = 1,\Lambda,N_{sfr}$ that are output from the
linear prediction coefficient conversion circuit 5030.
A weighting synthesis filter $H(z)W(z) = Q(z/\gamma_1)/[A(z)Q(z/\gamma_2)]$ having $\alpha_j^{(m)}(n)$ and $\hat{\alpha}_j^{(m)}(n)$ is driven by the
excitation vector to obtain a weighted reconstructed
15   vector. The transfer function $H(z) = 1/A(z)$ of the
synthesis filter is given by $1/A(z) = 1/(1-\sum_{i=1}^{N_p}\hat{\alpha}_i^{(m)}z^i)$.

The subtractor 5060 receives the weighted
input vector output from the weighting filter 5050 and
the weighted reconstructed vector output from the
20   weighting synthesis filter 5040, calculates their
difference, and outputs it as a difference vector to a
minimizing circuit 5070.

The minimizing circuit 5070 sequentially
outputs all indices corresponding to sound source
25   vectors stored in a sound source signal generation

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circuit 5110 to the sound source signal generation circuit 5110. The minimizing circuit 5070 sequentially outputs indices corresponding to all delays L_{pd} within a range defined by a pitch signal generation circuit 5210 to the pitch signal generation circuit 5210. The minimizing circuit 5070 sequentially outputs indices corresponding to all first gains stored in a first gain generation circuit 6220 to the first gain generation circuit 6220, and indices corresponding to all second gains stored in a second gain generation circuit 6120 to the second gain generation circuit 6120.

The minimizing circuit 5070 sequentially receives difference vectors output from the subtractor 5060, calculates their norms, selects a sound source vector, delay L_{pd}, and first and second gains that minimize the norm, and outputs corresponding indices to the code output circuit 6010. The pitch signal generation circuit 5210, sound source signal generation circuit 5110, first gain generation circuit 6220, and second gain generation circuit 6120 sequentially receive indices output from the minimizing circuit 5070.

The pitch signal generation circuit 5210, sound source signal generation circuit 5110, first gain generation circuit 6220, and second gain generation circuit 6120 are the same as the pitch signal decoding circuit 1210, sound source signal decoding circuit 1110, first gain decoding circuit 1220, and second gain

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decoding circuit 1120 in Fig. 4 except for input/output connections, and a detailed description of these blocks will be omitted.

The code output circuit 6010 receives an index corresponding to the quantized LSP output from the LSP conversion/quantization circuit 5520, and indices corresponding to the sound source vector, delay L_{pd}, and first and second gains that are output from the minimizing circuit 5070. The code output circuit 6010 converts these indices into a bit stream code, and outputs it via an output terminal 40.

The first problem is that sound different from normal voiced speech is generated in short unvoiced speech intermittently contained in the voiced speech or part of the voiced speech. As a result, discontinuous sound is generated in the voiced speech. This is because the LSP variation amount $d_0(m)$ decreases in the short unvoiced speech to increase the smoothing coefficient. Since $d_0(m)$ greatly varies over time, $d_0(m)$ exhibits a large value to a certain degree in part of the voiced speech, but the smoothing coefficient does not become 0.

The second problem is that the smoothing coefficient abruptly changes in unvoiced speech. As a result, discontinuous sound is generated in the unvoiced speech. This is because the smoothing coefficient is determined using $d_0(m)$ which greatly varies over time.

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The third problem is that proper smoothing processing corresponding to the type of background noise cannot be selected. As a result, the decoding quality degrades. This is because the decoding parameter is

5   smoothed based on a single algorithm using only different set parameters.

Summary of the Invention

It is an object of the present invention to provide a speech signal decoding method and apparatus

10   for improving the quality of reconstructed speech against background noise speech.

To achieve the above object, according to the present invention, there is provided a speech signal decoding method comprising the steps of decoding

15   information containing at least a sound source signal, a gain, and filter coefficients from a received bit stream, identifying voiced speech and unvoiced speech of a speech signal using the decoded information, performing smoothing processing based on the decoded information

20   for at least either one of the decoded gain and the decoded filter coefficients in the unvoiced speech, and decoding the speech signal by driving a filter having the decoded filter coefficients by an excitation signal obtained by multiplying the decoded sound source signal

25   by the decoded gain using a result of the smoothing processing.

Brief Description of the Drawings

Fig. 1 is a block diagram showing a speech signal decoding apparatus according to the first embodiment of the present invention;

Fig. 2 is a block diagram showing a speech signal decoding apparatus according to the second embodiment of the present invention;

Fig. 3 is a block diagram showing a speech signal encoding apparatus used in the present invention;

Fig. 4 is a block diagram showing a conventional speech signal decoding apparatus; and

Fig. 5 is a block diagram showing a conventional speech signal encoding apparatus.

Description of the Preferred Embodiments

The present invention will be described in detail below with reference to the accompanying drawings.

Fig. 1 shows a speech signal decoding apparatus according to the first embodiment of the present invention. An input terminal 10, output terminal 20, LSP decoding circuit 1020, linear prediction coefficient conversion circuit 1030, sound source signal decoding circuit 1110, storage circuit 1240, pitch signal decoding circuit 1210, first gain circuit 1230, second gain circuit 1130, adder 1050, and synthesis filter 1040 are the same as the blocks described in the prior art of Fig. 4, and a description thereof will be omitted.

A code input circuit 1010, voiced/unvoiced identification circuit 2020, noise classification circuit 2030, first switching circuit 2110, second switching circuit 2210, first filter 2150, second filter 2160, third filter 2170, fourth filter 2250, fifth filter 2260, sixth filter 2270, first gain decoding circuit 2220, and second gain decoding circuit 2120 will be described.

A bit stream is input at a period (frame) of T_{fr} msec (e.g., 20 msec), and a reconstructed vector is calculated at a period (subframe) of T_{fr}/N_{sfr} msec (e.g., 5 msec) for an integer N_{sfr} (e.g., 4). The frame length is given by L_{fr} samples (e.g., 320 samples), and the subframe length is given by L_{sfr} samples (e.g., 80 samples). These numbers of samples are determined by the sampling frequency (e.g., 16 kHz) of an input signal.

Each block will be described.

The code input circuit 1010 segments the code of a bit stream input from an input terminal 10 into several segments, and converts them into indices corresponding to a plurality of decoding parameters. The code input circuit 1010 outputs an index corresponding to LSP to the LSP decoding circuit 1020. The circuit 1010 outputs an index corresponding to a speech mode to a speech mode decoding circuit 2050, an index corresponding to a frame energy to a frame power decoding circuit 2040, an index corresponding to a delay

L_{pd} to the pitch signal decoding circuit 1210, and an index corresponding to a sound source vector to the sound source signal decoding circuit 1110. The circuit 1010 outputs an index corresponding to the first gain to

5   the first gain decoding circuit 2220, and an index corresponding to the second gain to the second gain decoding circuit 2120.

The speech mode decoding circuit 2050 receives the index corresponding to the speech mode that is

10 output from the code input circuit 1010, and sets a speech mode S_{mode} corresponding to the index. The speech mode is determined by threshold processing for an intra-frame average $\overline{G}_{op}(n)$ of an open-loop pitch prediction gain $G_{op}(m)$ calculated using a perceptually

15 weighted input signal in a speech encoder. The speech mode is transmitted to the decoder. In this case, n represents the frame number; and m, the subframe number. Determination of the speech mode is described in K. Ozawa et al., "M-LCELP Speech Coding at 4 kb/s with

20 Multi-Mode and Multi-Codebook", IEICE Trans. On Commun., Vol. E77-B, No. 9, pp. 1114 - 1121, September 1994 (reference 3).

The speech mode decoding circuit 2050 outputs the speech mode S_{mode} to the voiced/unvoiced

25 identification circuit 2020, first gain decoding circuit 2220, and second gain decoding circuit 2120.

The frame power decoding circuit 2040 has a

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table 2040a which stores a plurality of frame energies. The frame power decoding circuit 2040 receives the index corresponding to the frame power that is output from the code input circuit 1010, and reads a frame power $\hat{E}_{rms}$ corresponding to the index from the table 2040a. The frame power is attained by quantizing the power of an input signal in the speech encoder, and an index corresponding to the quantized value is transmitted to the decoder. The frame power decoding circuit 2040 outputs the frame power $\hat{E}_{rms}$ to the voiced/unvoiced identification circuit 2020, first gain decoding circuit 2220, and second gain decoding circuit 2120.

The voiced/unvoiced identification circuit 2020 receives LSP$\hat{q}_j^{(m)}(n)$ output from the LSP decoding circuit 1020, the speech mode S_{mode} output from the speech mode decoding circuit 2050, and the frame power $\hat{E}_{rms}$ output from the frame power decoding circuit 2040. The sequence of obtaining the variation amount of a spectral parameter will be explained.

As the spectral parameter, LSP$\hat{q}_j^{(m)}(n)$ is used. In the nth frame, a long-term average $\overline{q}_j(n)$ of the LSP is calculated by

$$\overline{q}_j(n) = \beta_0 \cdot \overline{q}_j(n - 1) + (1 - \beta_0) \cdot \hat{q}_j^{(N_{sfr})}(n) , j = 1, \Lambda, N_p$$

where $\beta_0 = 0.9$.

A variation amount $d_q(n)$ of the LSP in the nth frame is defined by

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$$d_q(n) = \sum_{j=1}^{N_p} \sum_{m=1}^{N_{sfr}} \frac{D_{q,j}^{(m)}(n)}{\overline{q}_j(n)}$$

where $D_{q,j}^{(m)}(n)$ corresponds to the distance between $\overline{q}_j(n)$ and $\hat{q}_j^{(m)}(n)$. For example,

$$D_{q,j}^{(m)}(n) = (\overline{q}_j(n) - \hat{q}_j^{(m)}(n))^2$$

or

$$D_{q,j}^{(m)}(n) = \left|\overline{q}_j(n) - \hat{q}_j^{(m)}(n)\right|$$

In this case, $D_{q,j}^{(m)}(n) = \left|\overline{q}_j(n) - \hat{q}_j^{(m)}(n)\right|$ is employed.

A section where the variation amount $d_q(n)$ is large substantially corresponds to voiced speech, whereas a section where the variation amount $d_q(n)$ is small substantially corresponds to unvoiced speech. However, the variation amount $d_q(n)$ greatly varies over time, and the range of $d_q(n)$ in voiced speech and that in unvoiced speech overlap each other. Thus, a threshold for identifying voiced speech and unvoiced speech is difficult to set.

For this reason, the long-term average of $d_q(n)$ is used to identify voiced speech and unvoiced speech. A long-term average $\overline{d}_{ql}(n)$ of $d_q(n)$ is calculated using a linear or non-linear filter. As $\overline{d}_{ql}(n)$, the average, median, or mode of $d_q(n)$ can be applied. In this case,

$$\overline{d}_{ql}(n) = \beta_1 \cdot \overline{d}_{ql}(n-1) + (1 - \beta_1) \cdot d_q(n)$$

is used where $\beta_1 = 0.9$.

Threshold processing for $\overline{d}_{ql}(n)$ determines an identification flag S_{vs}:

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if $(\overline{d}_{ql}(n) \geq C_{thl})$ then $S_{vs} = 1$

else $S_{vs} = 0$

where C_{thl} is a given constant (e.g., 2.2), $S_{vs} = 1$ corresponds to voiced speech, and $S_{vs} = 0$ corresponds to

5   unvoiced speech.

Even voiced speech may be mistaken for unvoiced speech in a section where steadiness is high because $d_q(n)$ is small. To avoid this, a section where the frame power and pitch prediction gain are large is

10 regarded as voiced speech. For $S_{vs} = 0$, S_{vs} is corrected by the following additional determination:

if $(\hat{E}_{rms} \geq C_{rms}$ and $S_{mode} \geq 2)$ then $S_{vs} = 1$

else $S_{vs} = 0$

where C_{rms} is a given constant (e.g., 10,000), and $S_{mode} \geq$

15 2 corresponds to an intra-frame average $\overline{G}_{op}(n)$ of 3.5 dB or more for the pitch prediction gain.

This is defined by the encoder.

The voiced/unvoiced identification circuit 2020 outputs S_{vs} to the noise classification circuit 2030,

20 first switching circuit 2110, and second switching circuit 2210, and $\overline{d}_{ql}(n)$ to the noise classification circuit 2030.

The noise classification circuit 2030 receives $\overline{d}_{ql}(n)$ and S_{vs} that are output from the voiced/unvoiced

25 identification circuit 2020. In unvoiced speech (noise), a value $\overline{d}_{q2}(n)$ which reflects the average behavior of $\overline{d}_{ql}(n)$ is obtained using a linear or non-linear filter.

For $S_{vs} = 0$,

$$\overline{d}_{q2}(n) = \beta_2 \cdot \overline{d}_{q2}(n-1) + (1 - \beta_2) \cdot \overline{d}_{q1}(n)$$

is calculated for $\beta_2 = 0.94$.

Threshold processing for $\overline{d}_{q2}(n)$ classifies

5   noise to determine a classification flag S_{nz}:

if $(\overline{d}_{q2}(n) \geq C_{th2})$ then $S_{nz} = 1$

else $S_{nz} = 0$

where C_{th2} is a given constant (e.g., 1.7), $S_{nz} = 1$

corresponds to noise whose frequency characteristics

10   unsteadily change over time, and $S_{nz} = 0$ corresponds to

noise whose frequency characteristics steadily change

over time. The noise classification circuit 2030

outputs S_{nz} to the first and second switching circuits

2110 and 2210.

15   The first switching circuit 2110 receives

$LSP\hat{q}_j^{(m)}(n)$ output from the LSP decoding circuit 1020, the

identification flag S_{vs} output from the voiced/unvoiced

identification circuit 2020, and the classification flag

S_{nz} output from the noise classification circuit 2030.

20   The first switching circuit 2110 is switched in

accordance with the identification and classification

flag values to output $LSP\hat{q}_j^{(m)}(n)$ to the first filter 2150

for $S_{vs} = 0$ and $S_{nz} = 0$, to the second filter 2160 for S_{vs}

$= 0$ and $S_{nz} = 1$, and to the third filter 2170 for $S_{vs} = 1$.

25   The first filter 2150 receives $LSP\hat{q}_j^{(m)}(n)$ output

from the first switching circuit 2110, smoothes it using

a linear or non-linear filter, and outputs it as a first

smoothed LSP$\overline{q}_{1,j}^{(m)}(n)$ to the linear prediction coefficient

conversion circuit 1030. In this case, the first filter

2150 uses a filter given by

$$\overline{q}_{1,j}^{(m)}(n) = \gamma_1 \cdot \overline{q}_{1,j}^{(m-1)}(n) + (1 - \gamma_1) \cdot \hat{q}_j^{(m)}(n) , \; j = 1, \Lambda, N_p$$

5   where $\overline{q}_{1,j}^{(0)}(n) = \overline{q}_{1,j}^{(N_{sfr})}(n - 1)$, and $\gamma_1 = 0.5$.

The second filter 2160 receives LSP$\hat{q}_j^{(m)}(n)$

output from the first switching circuit 2110, smoothes

it using a linear or non-linear filter, and outputs it

as a second smoothed LSP$\overline{q}_{2,j}^{(m)}(n)$ to the linear prediction

10   coefficient conversion circuit 1030. In this case, the

second filter 2160 uses a filter given by

$$\overline{q}_{2,j}^{(m)}(n) = \gamma_2 \cdot \overline{q}_{2,j}^{(m-1)}(n) + (1 - \gamma_2) \cdot \hat{q}_j^{(m)}(n) , \; j = 1, \Lambda, N_p$$

where $\overline{q}_{2,j}^{(0)}(n) = \overline{q}_{2,j}^{(N_{sfr})}(n - 1)$, and $\gamma_1 = 0.0$.

The third filter 2170 receives LSP$\hat{q}_j^{(m)}(n)$ output

15   from the first switching circuit 2110, smoothes it using

a linear or non-linear filter, and outputs it as a third

smoothed LSP$\overline{q}_{3,j}^{(m)}(n)$ to the linear prediction coefficient

conversion circuit 1030. In this case, $\overline{q}_{3,j}^{(m)}(n) = \hat{q}_j^{(m)}(n)$.

The second switching circuit 2210 receives the

20   second gain $\hat{g}_2^{(m)}(n)$ output from the second gain decoding

circuit 2120, the identification flag S_{vs} output from the

voiced/unvoiced identification circuit 2020, and the

classification flag S_{nz} output from the noise

classification circuit 2030. The second switching

25   circuit 2210 is switched in accordance with the

identification and classification flag values to output

the second gain $\hat{g}_2^{(m)}(n)$ to the fourth filter 2250 for $S_{vs} = $

0 and $S_{nz} = 0$, to the fifth filter 2260 for $S_{vs} = 0$ and

$S_{nz} = 1$, and to the sixth filter 2270 for $S_{vs} = 1$.

The fourth filter 2250 receives the second

gain $\hat{g}_2^{(m)}(n)$ output from the second switching circuit 2210,

5 smoothes it using a linear or non-linear filter, and

outputs it as a first smoothed gain $\overline{g}_{2,1}^{(m)}(n)$ to the second

gain circuit 1130. In this case, the fourth filter 2250

uses a filter given by

$$\overline{g}_{2,1}^{(m)}(n) = \gamma_2 \cdot \overline{g}_{2,1}^{(m-1)}(n) + (1 - \gamma_2) \cdot \hat{g}_2^{(m)}(n)$$

10 where $\overline{g}_{2,1}^{(0)}(n) = \overline{g}_{2,1}^{(N_{sfr})}(n - 1)$, and $\gamma_2 = 0.9$.

The fifth filter 2260 receives the second gain

$\hat{g}_2^{(m)}(n)$ output from the second switching circuit 2210,

smoothes it using a linear or non-linear filter, and

outputs it as a second smoothed gain $\overline{g}_{2,2}^{(m)}(n)$ to the second

15 gain circuit 1130. In this case, the fifth filter 2260

uses a filter given by

$$\overline{g}_{2,2}^{(m)}(n) = \gamma_2 \cdot \overline{g}_{2,2}^{(m-1)}(n) + (1 - \gamma_2) \cdot \hat{g}_2^{(m)}(n)$$

where $\overline{g}_{2,2}^{(0)}(n) = \overline{g}_{2,2}^{(N_{sfr})}(n - 1)$, and $\gamma_2 = 0.9$.

The sixth filter 2270 receives the second gain

20 $\hat{g}_2^{(m)}(n)$ output from the second switching circuit 2210,

smoothes it using a linear or non-linear filter, and

outputs it as a third smoothed gain $\overline{g}_{2,3}^{(m)}(n)$ to the second

gain circuit 1130. In this case, $\overline{g}_{2,3}^{(m)}(n) = \hat{g}_2^{(m)}(n)$.

The first gain decoding circuit 2220 has a

25 table 2220a which stores a plurality of gains. The

first gain decoding circuit 2220 receives an index

corresponding to the third gain output from the code

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input circuit 1010, the speech mode S_{mode} output from the speech mode decoding circuit 2050, the frame power $\hat{E}_{rms}$ output from the frame power decoding circuit 2040, the linear prediction coefficient $\hat{\alpha}_j^{(m)}(n)$, $j = 1, \Lambda, N_p$ of the

5   mth subframe of the nth frame output from the linear prediction coefficient conversion circuit 1030, and a pitch vector $c_{ac}(i)$, $i = 1, \Lambda, L_{sfr}$ output from the pitch signal decoding circuit 1210.

The first gain decoding circuit 2220

10   calculates a k parameter $k_j^{(m)}(n)$, $j = 1, \Lambda, N_p$ (to be simply represented as k_j) from the linear prediction coefficient $\hat{\alpha}_j^{(m)}(n)$. This is calculated by a known method, e.g., a method described in Section 8.3.2 in L.R. Rabiner et al., "Digital Processing of Speech Signals",

15   Prentice-Hall, 1978 (reference 4). Then, the first gain decoding circuit 2220 calculates an estimated residual power $\tilde{E}_{res}$ using k_j:

$$\tilde{E}_{res} = \hat{E}_{rms}\sqrt{\Pi_{j=1}^{N_p}(1 - k_j^2)}$$

The first gain decoding circuit 2220 reads a

20   third gain $\hat{\gamma}_{gac}$ corresponding to the index from the table 2220a switched by the speech mode S_{mode}, and calculates a first gain $\hat{g}_{ac}$:

$$\hat{g}_{ac} = \hat{\gamma}_{gac} \frac{\tilde{E}_{res}}{\sqrt{\sum_{i=0}^{L_{sfr}-1} c_{ac}^2(i)}}$$

The first gain decoding circuit 2220 outputs

25   the first gain $\hat{g}_{ac}$ to the first gain circuit 1230. The second gain decoding circuit 2120 has a table 2120a

which stores a plurality of gains.

The second gain decoding circuit 2120 receives an index corresponding to the fourth gain output from the code input circuit 1010, the speech mode S_{mode} output from the speech mode decoding circuit 2050, the frame power $\hat{E}_{rms}$ output from the frame power decoding circuit 2040, the linear prediction coefficient $\hat{\alpha}_j^{(m)}(n)$, $j = 1, \Lambda, N_p$ of the mth subframe of the nth frame output from the linear prediction coefficient conversion circuit 1030, and a sound source vector $c_{ec}(i)$, $i = 1, \Lambda, L_{sfr}$ output from the sound source signal decoding circuit 1110.

The second gain decoding circuit 2120 calculates a k parameter $k_j^{(m)}(n)$, $j = 1, \Lambda, N_p$ (to be simply represented as k_j) from the linear prediction coefficient $\hat{\alpha}_j^{(m)}(n)$. This is calculated by the same known method as described for the first gain decoding circuit 2220. Then, the second gain decoding circuit 2120 calculates an estimated residual power $\tilde{E}_{res}$ using k_j:

$$\tilde{E}_{res} = \hat{E}_{rms}\sqrt{\Pi_{j=1}^{N_p}(1 - k_j^2)}$$

The second gain decoding circuit 2120 reads a fourth gain $\hat{\gamma}_{gec}$ corresponding to the index from the table 2120a switched by the speech mode S_{mode}, and calculates a second gain $\hat{g}_{ec}$:

$$\hat{g}_{ec} = \hat{\gamma}_{gec} \frac{\tilde{E}_{res}}{\sqrt{\sum_{i=0}^{L_{sfr}-1} c_{ec}^2(i)}}$$

The second gain decoding circuit 2120 outputs

the second gain $\hat{g}_{ec}$ to the second switching circuit 2210.

Fig. 2 shows a speech signal decoding apparatus according to the second embodiment of the present invention.

This speech signal decoding apparatus of the present invention is implemented by replacing the frame power decoding circuit 2040 in the first embodiment with a power calculation circuit 3040, the speech mode decoding circuit 2050 with a speech mode determination circuit 3050, the first gain decoding circuit 2220 with a first gain decoding circuit 1220, and the second gain decoding circuit 2120 with second gain decoding circuit 1120. In this arrangement, the frame power and speech mode are not encoded and transmitted in the encoder, and the frame power (power) and speech mode are obtained using parameters used in the decoder.

The first and second gain decoding circuits 1220 and 1120 are the same as the blocks described in the prior art of Fig. 4, and a description thereof will be omitted.

The power calculation circuit 3040 receives a reconstructed vector output from a synthesis filter 1040, calculates a power from the sum of squares of the reconstructed vectors, and outputs the power to a voiced/unvoiced identification circuit 2020. In this case, the power is calculated for each subframe. Calculation of the power in the mth subframe uses a

reconstructed signal output from the synthesis filter 1040 in the (m-1)th subframe. For a reconstructed signal $S_{syn}(i)$, $i = 0, \Lambda, L_{sfr}$, the power E_{rms} is calculated by, e.g., RMS (Root Mean Square):

5

$$E_{rms} = \sqrt{\sum_{i=0}^{L_{sfr}-1} s_{syn}^2(i)}$$

The speech mode determination circuit 3050 receives a past excitation vector $e_{mem}(i)$, $i = 0, \Lambda, L_{mem}-1$ held by a storage circuit 1240, and the index output from the code input circuit 1010. The index designates

10   a delay L_{pd}. L_{mem} is a constant determined by the maximum value of L_{pd}.

In the mth subframe, a pitch prediction gain $G_{emem}(m)$, $m = 1, \Lambda, N_{sfr}$ is calculated from the past excitation vector $e_{mem}(i)$ and delay L_{pd}:

15   $$G_{emem}(m) = 10 \cdot \log_{10}(g_{emem}(m))$$

where

$$g_{emem}(m) = \frac{1}{1 - \dfrac{E_c^2(m)}{E_{a1}(m)E_{a2}(m)}}$$

$$E_{a1}(m) = \sum_{i=0}^{L_{sfr}-1} e_{mem}^2(i)$$

$$E_{a2}(m) = \sum_{i=0}^{L_{sfr}-1} e_{mem}^2(i - L_{pd})$$

20   $$E_c(m) = \sum_{i=0}^{L_{sfr}-1} e_{mem}(i)e_{mem}(i - L_{pd})$$

The pitch prediction gain $G_{emem}(m)$ or the intra-frame average $\overline{G}_{emem}(n)$ in the nth frame of $G_{emem}(m)$ undergoes the following threshold processing to set a

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speech mode S_{mode}:

  if ($\overline{G}_{emem}(n) \geq 3.5$) then $S_{mode} = 2$

  else $S_{mode} = 0$

The speech mode determination circuit 3050 outputs the
speech mode S_{mode} to the voiced/unvoiced identification
circuit 2020.

Fig. 3 shows a speech signal encoding
apparatus used in the present invention.

The speech signal encoding apparatus in Fig. 3
is implemented by adding a frame power calculation
circuit 5540 and speech mode determination circuit 5550
in the prior art of Fig. 5, replacing the first and
second gain generation circuits 6220 and 6120 with first
and second gain generation circuits 5220 and 5120, and
replacing the code output circuit 6010 with a code
output circuit 5010. The first and second gain
generation circuits 5220 and 5120, an adder 1050, and a
storage circuit 1240 are the same as the blocks
described in the prior art of Fig. 5, and a description
thereof will be omitted.

The frame power calculation circuit 5540 has a
table 5540a which stores a plurality of frame energies.
The frame power calculation circuit 5540 receives an
input vector from an input terminal 30, calculates the
RMS (Root Mean Square) of the input vector, and
quantizes the RMS using the table to attain a quantized
frame power $\hat{E}_{rms}$. For an input vector $s_i(i)$, $i = 0, \Lambda, L_{sfr}$,

a power E_{irms} is given by

$$E_{irms} = \sqrt{\sum_{i=0}^{L_{sfr}-1} s_i^2(i)}$$

The frame power calculation circuit 5540 outputs the quantized frame power $\hat{E}_{rms}$ to the first and

5   second gain generation circuits 5220 and 5120, and an index corresponding to $\hat{E}_{rms}$ to the code output circuit 5010.

The speech mode determination circuit 5550 receives a weighted input vector output from a weighting

10   filter 5050.

The speech mode S_{mode} is determined by executing threshold processing for the intra-frame average $\overline{G}_{op}(n)$ of an open-loop pitch prediction gain $G_{op}(m)$ calculated using the weighted input vector.   In

15   this case, n represents the frame number; and m, the subframe number.

In the mth subframe, the following two equations are calculated from a weighted input vector $s_{wi}(i)$ and the delay L_{tmp}, and L_{tmp} which maximizes

20   $E_{sctmp}^2(m) / E_{sa2tmp}$ is obtained and set as L_{op}:

$$E_{sctmp}(m) = \sum_{i=0}^{L_{sfr}-1} s_{wi}(i)s_{wi}(i - L_{tmp})$$

$$E_{sa2tmp}(m) = \sum_{i=0}^{L_{sfr}-1} s_{wi}^2(i - L_{tmp})$$

From the weighted input vector $s_{wi}(i)$ and the delay L_{op}, the pitch prediction gain $G_{op}(m)$, $m = 1, \Lambda, N_{sfr}$

25   is calculated:

$$G_{op}(m) = 10 \cdot \log_{10}(g_{op}(m))$$

where

where

$$g_{op}(m) = \cfrac{1}{1 - \cfrac{E_{sc}^2(m)}{E_{sa1}(m)E_{sa2}(m)}}$$

$$E_{sa1}(m) = \sum_{i=0}^{L_{sfr}-1} s_{wi}^2(i)$$

$$E_{sa2}(m) = \sum_{i=0}^{L_{sfr}-1} s_{wi}^2(i - L_{op})$$

$$E_{sc}(m) = \sum_{i=0}^{L_{sfr}-1} s_{wi}(i)s_{wi}(i - L_{op})$$

The pitch prediction gain $G_{op}(m)$ or the intra-frame average $\overline{G}_{op}(n)$ in the nth frame of $G_{op}(m)$ undergoes the following threshold processing to set the speech mode S_{mode}:

$$\text{if } (\overline{G}_{op}(n) \geq 3.5) \text{ then } S_{mode} = 2$$

$$\text{else } S_{mode} = 0$$

Determination of the speech mode is described in K. Ozawa et al., "M-LCELP Speech Coding at 4 kb/s with Multi-Mode and Multi-Codebook", IEICE Trans. On Commun., Vol. E77-B, No. 9, pp. 1114 - 1121, 1994 (reference 3).

The speech mode determination circuit 5550 outputs the speech mode S_{mode} to the first and second gain generation circuits 5220 and 5120, and an index corresponding to the speech mode S_{mode} to the code output circuit 5010.

A pitch signal generation circuit 5210, a

sound source signal generation circuit 5110, and the first and second gain generation circuits 5220 and 5120 sequentially receive indices output from a minimizing circuit 5070. The pitch signal generation circuit 5210, sound source signal generation circuit 5110, first gain generation circuit 5220, and second gain generation circuit 5120 are the same as the pitch signal decoding circuit 1210, sound source signal decoding circuit 1110, first gain decoding circuit 2220, and second gain decoding circuit 2120 in Fig. 1 except for input/output connections, and a detailed description of these blocks will be omitted.

The code output circuit 5010 receives an index corresponding to the quantized LSP output from the LSP conversion/quantization circuit 5520, an index corresponding to the quantized frame power output from the frame power calculation circuit 5540, an index corresponding to the speech mode output from the speech mode determination circuit 5550, and indices corresponding to the sound source vector, delay L_{pd}, and first and second gains that are output from the minimizing circuit 5070. The code output circuit 5010 converts these indices into a bit stream code, and outputs it via an output terminal 40.

The arrangement of a speech signal encoding apparatus in a speech signal encoding/decoding apparatus according to the fourth embodiment of the present

invention is the same as that of the speech signal encoding apparatus in the conventional speech signal encoding/decoding apparatus, and a description thereof will be omitted.

In the above-described embodiments, the long-term average of $d_0(m)$ varies over time more gradually than $d_0(m)$, and does not intermittently decrease in voiced speech. If the smoothing coefficient is determined in accordance with this average, discontinuous sound generated in short unvoiced speech intermittently contained in voiced speech can be reduced. By performing identification of voiced or unvoiced speech using the average, the smoothing coefficient of the decoding parameter can be completely set to 0 in voiced speech.

Also for unvoiced speech, using the long-term average of $d_0(m)$ can prevent the smoothing coefficient from abruptly changing.

The present invention smoothes the decoding parameter in unvoiced speech not by using single processing, but by selectively using a plurality of processing methods prepared in consideration of the characteristics of an input signal. These methods include moving average processing of calculating the decoding parameter from past decoding parameters within a limited section, auto-regressive processing capable of considering long-term past influence, and non-linear

processing of limiting a preset value by an upper or lower limit after average calculation.

According to the first effect of the present invention, sound different from normal voiced speech that is generated in short unvoiced speech intermittently contained in voiced speech or part of the voiced speech can be reduced to reduce discontinuous sound in the voiced speech. This is because the long-term average of $d_0(m)$ which hardly varies over time is used in the short unvoiced speech, and because voiced speech and unvoiced speech are identified and the smoothing coefficient is set to 0 in the voiced speech.

According to the second effect of the present invention, abrupt changes in smoothing coefficient in unvoiced speech are reduced to reduce discontinuous sound in the unvoiced speech. This is because the smoothing coefficient is determined using the long-term average of $d_0(m)$ which hardly varies over time.

According to the third effect of the present invention, smoothing processing can be selected in accordance with the type of background noise to improve the decoding quality. This is because the decoding parameter is smoothed selectively using a plurality of processing methods in accordance with the characteristics of an input signal.